

**IN THE SPECIFICATION**

*Please replace the following paragraph beginning at page 10 line 9 and ending at page 10, line 22 with the following rewritten paragraph:*

A processor of Example 1 of the present invention shifts the control to the next program module, when an address of an executing instruction in a program module (process) under processing (current execution instruction address) is matched with an end address of the program module (~~hereinafter, referred to as "current execution instruction address"~~). Both the addresses are absolute addresses. Thus, branching can be performed without executing a branching instruction, so that high speed processing is possible. Furthermore, as for the next program module, the combination and the processing order of the program modules to be processed can be controlled flexibly by management (or determination) of a supervisory processor. This example will be described more specifically with reference to FIG. 1 below.